

#### INVENTOR INFORMATION

Inventor One Given Name:: MINORU  
Family Name:: IWAMOTO  
Postal Address Line One:: C/O NEC IC MICROCOMPUTER SYSTEMS,  
Postal Address Line Two:: LTD., 403-53, KOSUGIMACHI 1-CHOME,  
Postal Address Line Three:: NAKAHARA-KU, KAWASAKI-SHI  
City:: KANAGAWA  
Country:: JAPAN  
City of Residence:: KANAGAWA  
Country of Residence:: JAPAN  
Citizenship Country:: JAPAN

#### CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 000466  
Name Line One:: YOUNG & THOMPSON  
Address Line One:: 745 SOUTH 23RD STREET  
Address Line Two:: SECOND FLOOR  
City:: ARLINGTON  
State or Province:: VIRGINIA  
Country:: U.S.A.  
Postal or Zip Code:: 22202  
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#### APPLICATION INFORMATION

Title Line One:: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING  
Title Line Two:: FUNCTIONAL MACRO WITH IMPROVED POWER  
Title Line Three:: LINE CONNECTION STRUCTURE  
Total Drawing Sheets:: 16  
Formal Drawings?: YES  
Application Type:: UTILITY  
Docket Number:: PF-2802/NEC/US/mh

#### REPRESENTATIVE INFORMATION

Representative Customer Number:: 000466

#### PRIOR FOREIGN APPLICATION

Foreign Application One:: 2000-157932  
Filing Date:: MAY 29, 2000  
Country:: JAPAN  
Priority Claimed:: YES